

Notice of References Cited	Application/Control No. 09/684,529	Applicant(s)/Patent Under Reexamination LOGUE ET AL.	
	Examiner Eva Yi Zheng	Art Unit 2634	Page 1 of 1

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	B	US-6,101,197	08-2000	Keeth et al.	370/517
	C	US-2002/0029355	03-2002	Sakamoto et al.	713/503
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	M	US-			

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Alex Waizman; A Delay Lin Loop for Frequency Synthesis of De-Skewed Clock; Solid-State Circuit Conference, 1994 IEEE; page 298-299
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.